**FIG. 2**

FIG. 2

This detailed block diagram shows the internal structure of the **WIRELESS COMMUNICATION SYSTEM** (24). The system includes a **RECEIVER**, a **PROCESSOR**, a **TRANSMITTER**, a **MEMORY**, and an **I/O** (Input/Output) interface. The **RECEIVER** (36) receives an **IN** signal and sends data to the **PROCESSOR** (38). The **PROCESSOR** also receives data from the **MEMORY** (40) and sends data to the **TRANSMITTER** (42) and the **I/O** (44). The **TRANSMITTER** sends an **OUT** signal. The **MEMORY** (40) is connected to both the **PROCESSOR** and the **I/O** (44).

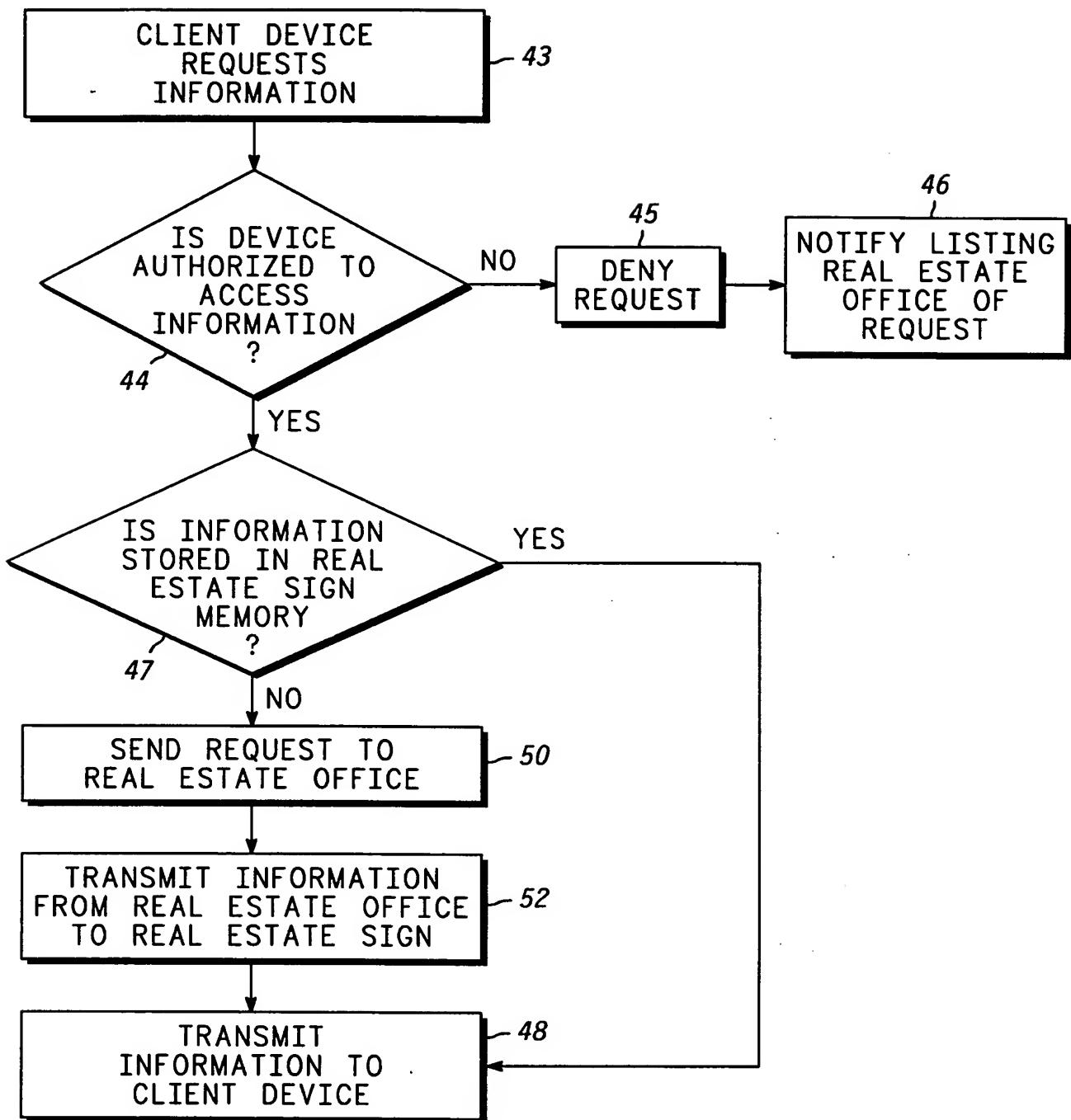
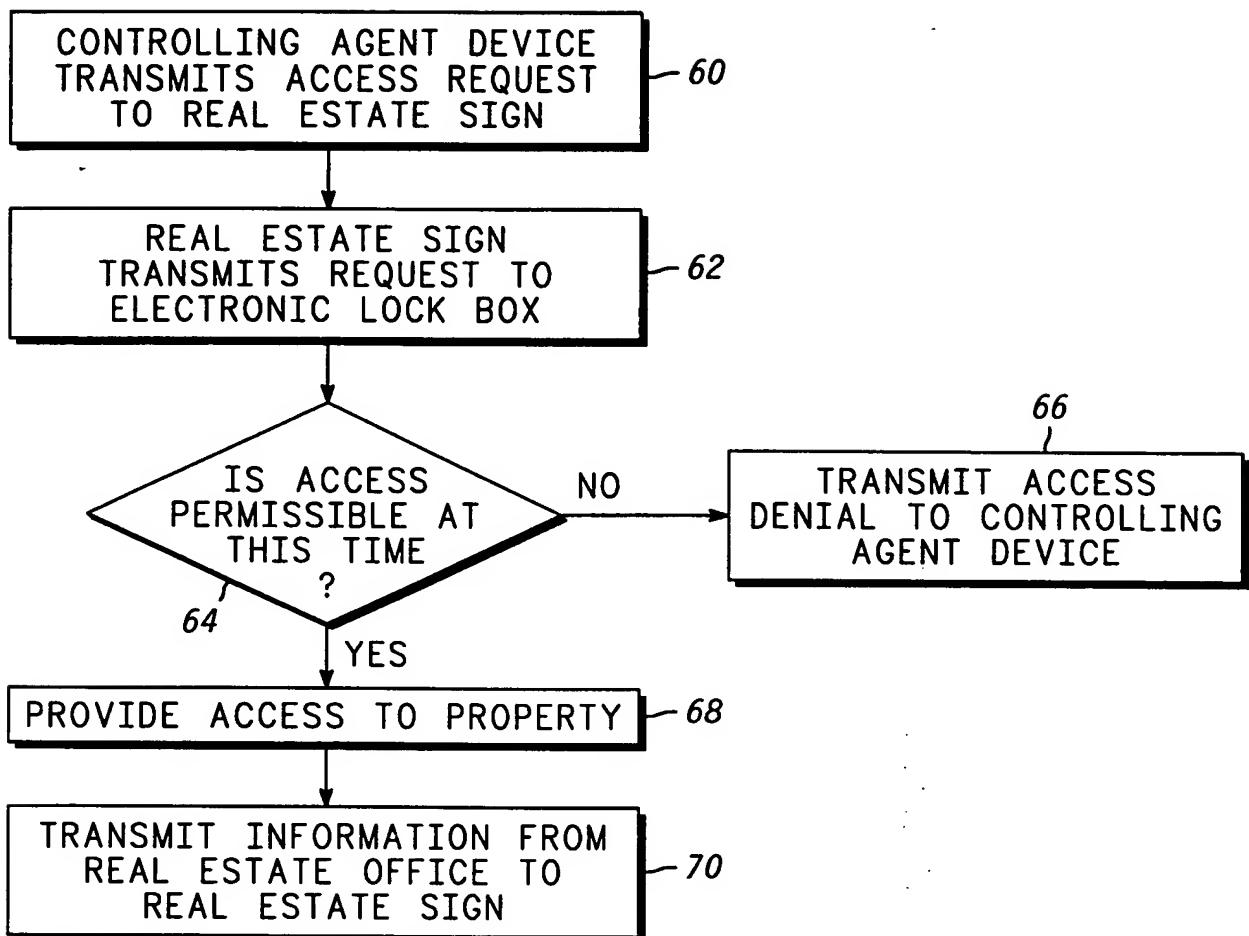


FIG. 3

***FIG. 4******FIG. 5***